

Omnidirectional Microphone with Bottom Port and Analog Output

Preliminary Technical Data

FEATURES

4.72 mm × 3.76 mm × 1.0 mm surface mount package High SNR: 62 dBA High sensitivity: -37 dBV Flat frequency response from 100 Hz to over 12 kHz Low current consumption: <200 μA Single-ended analog output High PSRR of >50 dB Compatible with Sn/Pb and Pb-free solder processes RoHS/WEEE compliant

APPLICATIONS

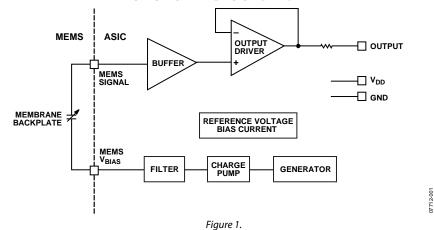
Cell phones PC audio Digital cameras Bluetooth headsets

GENERAL DESCRIPTION

The ADMP401-1 is a high quality, low cost, low power analog output bottom-ported omnidirectional MEMS microphone. The ADMP401-1 consists of a MEMS microphone element and an output amplifier. The ADMP401-1 has a high SNR and high sensitivity, making it an excellent choice for far field applications. The ADMP401-1 has a flat wideband frequency response resulting in natural sound with high intelligibility. Low current consumption enables long battery life for portable applications. A built-in particle filter provides for high reliability.

ADMP401-1

The ADMP401-1 is available in a thin 4.72 mm \times 3.76 mm \times 1.0 mm surface mount package. The ADMP401-1 is reflow solder compatible with no sensitivity degradation.



FUNCTIONAL BLOCK DIAGRAM

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SPECIFICATIONS

 $T_A = 25^{\circ}$ C, $V_{DD} = 1.8$ V, unless otherwise noted. All minimum and maximum specifications are guaranteed. Typical specifications are not guaranteed.

Table 1.

| Parameter | Symbol | Test Conditions/Comments | Min | Тур | Max | Unit |
|------------------------------|-----------------|--|---------------------------|------|-----|---------|
| PERFORMANCE | | | | | | |
| Directionality | | | | Omni | | |
| Sensitivity | | 1 kHz, 94 dB SPL | -40 | -37 | -34 | dBV |
| Signal-to-Noise Ratio | SNR | | 60 | 62 | | dBA |
| Equivalent Input Noise | EIN | | | 32 | | dBA SPL |
| Frequency Response | | 100 Hz to 10 kHz | -2 | 0 | +2 | dB |
| | | 100 Hz to 12 kHz | -3 | 0 | +2 | dB |
| Total Harmonic Distortion | THD | 105 dB SPL | | | 3 | % |
| Power Supply Rejection Ratio | PSRR | 217 Hz, 100 mV p-p square wave super- imposed on $V_{DD} = 1.8 V$ | 50 | | | dB |
| Maximum Acoustic Input | | Peak | | 120 | | dB SPL |
| POWER SUPPLY | | | | | | |
| Supply Voltage | V _{DD} | | 1.5 | | 3.6 | V |
| Supply Current | ls | | | | 200 | μΑ |
| OUTPUT CHARACTERISTICS | | | | | | |
| Output Impedance | ZOUT | | | 200 | | Ω |
| Polarity | | | Noninverting ¹ | | | |

¹ Positive going (increasing) pressure on the membrane results in a positive going (increasing) output voltage.

ABSOLUTE MAXIMUM RATINGS

Table 2.

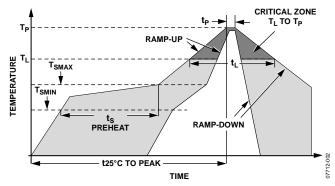
| Parameter | Rating |
|----------------------------|------------------|
| Supply Voltage | 3.6 V |
| Sound Pressure Level (SPL) | 160 dB |
| Mechanical Shock | 20,000 g |
| Vibration | Per MIL-STD-883G |
| Temperature Range | -40°C to +85°C |

Stresses above those listed under Absolute Maximum Ratings may cause permanent damage to the device. This is a stress rating only; functional operation of the device at these or any other conditions above those indicated in the operational section of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ESD CAUTION



ESD (electrostatic discharge) sensitive device. Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.



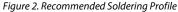


Table 3. Recommended Soldering Profile

| Profile Feature | Sn63/Pb37 | Pb-Free | |
|--|-------------------|-------------------|--|
| Average Ramp Rate $(T_L \text{ to } T_P)$ | 3°C/sec max | 3°C/sec max | |
| Preheat | | | |
| Minimum Temperature (T _{SMIN}) | 100°C | 150°C | |
| Maximum Temperature (T _{SMAX}) | 150°C | 200°C | |
| Time (T _{SMIN} to T _{SMAX}), ts | 60 sec to 120 sec | 60 sec to 120 sec | |
| Ramp-Up Rate (T _{SMAX} to T _L) | 3°C/sec | 3°C/sec | |
| Time Maintained Above Liquidous (t _L) | 60 sec to 150 sec | 60 sec to 150 sec | |
| Liquidous Temperature (T _L) | 183℃ | 217°C | |
| Peak Temperature (T _P) | 240°C + 0°C/–5°C | 260°C + 0°C/–5°C | |
| Time Within 5°C of Actual Peak Temperature (t _P) | 10 sec to 30 sec | 20 sec to 40 sec | |
| Ramp-Down Rate | 6°C/sec max | 6°C/sec max | |
| Time 25°C to Peak Temperature | 6 minute max | 8 minute max | |

PIN CONFIGURATION AND FUNCTION DESCRIPTIONS

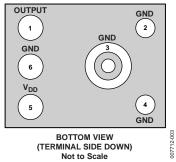


Figure 3. Pin Configuration (Bottom View)

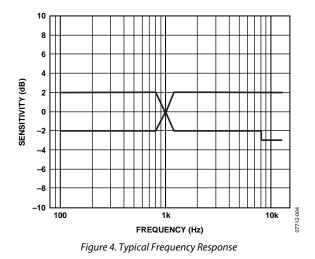
Table 4. Pin Function Descriptions

| Pin No. | Mnemonic | Description |
|---------|-----------------|-----------------------|
| 1 | OUTPUT | Analog Output Signal. |
| 2 | GND | Ground. |
| 3 | GND | Ground. |
| 4 | GND | Ground. |
| 5 | V _{DD} | Power Supply. |
| 6 | GND | Ground. |

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TYPICAL PERFORMANCE CHARACTERISTICS



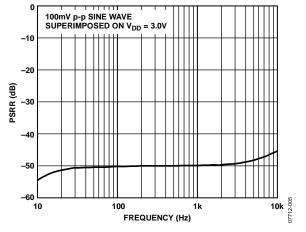


Figure 5. Typical Power Supply Rejection Ratio vs. Frequency

HANDLING INSTRUCTIONS PICK-AND-PLACE EQUIPMENT

The MEMS microphone can be handled using standard pickand-place and chip shooting equipment. Care should be taken to avoid damage to the MEMS microphone structure as follows:

- Use a standard pickup tool to handle the microphone. Because the microphone hole is on the bottom of the package, the pickup tool can make contact with any part of the lid surface.
- Use care during pick-and-place to ensure that no high shock events above 20 kg are experienced, because such events may cause damage to the microphone.
- Do not pick up the microphone with a vacuum tool that makes contact with the bottom side of the microphone. Do not pull air out or blow air into the microphone port.
- Do not use excessive force to place the microphone on the PCB.

REFLOW SOLDER

Standard reflow solder conditions specified in Figure 2 can be used to attach the MEMS microphone to the PCB.

BOARD WASH

When washing the PCB, ensure that water does not make contact with the microphone port. Blow-off procedures and ultrasonic cleaning must not be used.

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OUTLINE DIMENSIONS

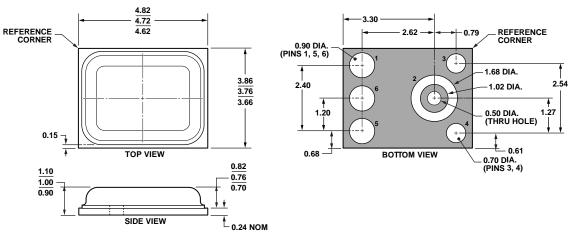


Figure 6. 6-Terminal Chip Array Small Outline No Lead Cavity [LGA_CAV] 4.72 mm × 3.76 mm Body (CE-6-1) Dimensions shown in millimeters

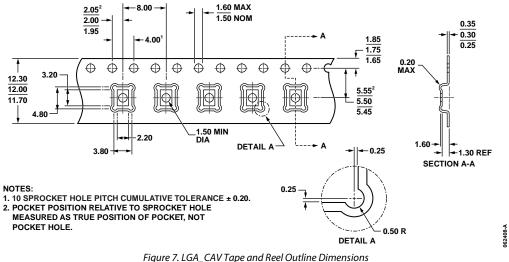


Figure 7. LGA_CAV Tape and Reel Outline Dimension. Dimensions shown in millimeters

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